

# 050-367

# PRODUCT BRIEF SIZE 8 ELECTRO-OPTICAL CONTACT TRANSMITTER SMPTE 3G-SDI AND HD-SDI FRONT INSERT – FRONT RELEASE

REV	DESCRIPTION	DATE	APPROVED
В	Per DCN60097	05/09/2016	SZ/NH/RS
С	Per DCN62204; Edit PRBS Specification to 2 <sup>7</sup> -1	10/20/2016	SZ/GC
D	Per DCN63108; Update Min. Extinction Ratio to 5dB per SMPTE Standard	12/13/2016	RAS/GC
E	Per DCN63598; Add part number options to distinguish between 3G-SDI and HD-SDI	01/24/2017	RAS/GC/SZ
F	Per DCN68837; Update receiver overload settings based on SMPTE ST 297:2015	01/26/2018	GC
G	Per DCN70674; General Update, update notes for differential input impedance	06/04/2018	GC
Н	Revised per DCN71938; Edit note on outline drawing to say "Maximum PCB thickness"	08/15/2018	RAS/GC
J	Revised per DCN75595; General Update; Picture, Interface Table	11/08/2019	YA/OAE
K	Revised per DCN88247; Update 2D drawing with New ASR and O-Ring	07/20/2020	RAS/MG
L	Revised per DCN91091; Removed receiver options	04/13/2022	SS

BF15U2-3711

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## Size 8 Cavity Opto-Electronic Contacts, 3G-SDI & HD-SDI, MMF, 3.3V



Glenair 050-367, Size 8 Cavity Opto-electronic contacts transmit differential CML electrical signals over Multimode fiber optic cable that has been optimized for SMPTE 3G-SDI and HD-SDI signals.

Transmitter size 8 contacts consist of a laser driver with a temperature compensation circuit to maintain optical power over the entire operating temperature range, and an 850nm VCSEL laser. The transmitter has a Tx Disable pin to turn off transmitter output and a Tx Fault pin to signal a fault condition. This laser driver circuit regulates the optical power at a constant level provided the data pattern is relatively DC balanced and can support SMPTE EG 34:2004 Pathological conditions CASE 1, CASE2, and CASE 3 for HD-SDI and 3G-SDI.

These contacts are designed to operate in harsh temperature and vibration environments found in Military, Aerospace, Railway, Oil and Gas, and Industrial applications. They can be incorporated into 38999 and other harsh environment connectors.

## **KEY FEATURES/BENEFITS**

- SMPTE EG 34:2004 Compliant to Pathological Conditions CASE 1, CASE 2 and CASE 3.
- SMPTE ST 297:2015 (3G-SDI & HD-SDI)
- SMPTE 424 Compliant (3G-SDI)
- SMPTE 292 Compliant (HD-SDI)
- SFP Compatible Electrical Input signal levels
- 850nm VCSEL support 3G-SDI & HD-SDI
- Industry standard CML input and outputs that make for simple integration on customer host PCB
- Front-release, front-insert, front-removable
- Fits size 8 quadrax cavity for ARINC 600

- Solutions available in 38999 style connectors
- -40°C to +85°C Operating Case Temperature
- Evaluation fixtures available
- ARINC 801 1.25mm ceramic fiber ferrule

#### APPLICATIONS

- Harsh Environment such as: Airborne, Tactical, Railway, Industrial, Oil and Gas and Shipboard applications
  - SMPTE 3G & HD-SDI Video Applications

## HOW TO ORDER

## Table 1Part Number Options

Part Number	Description
050-367-1-T	HD-SDI Transmitter
050-367-T	3G-SDI Transmitter

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## Ratings and Specifications – HD-SDI VERSION

### TABLE 2 ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Тур	Max	Units	Notes
Storage Temperature	Ts	-55		+100	°C	
Supply Voltage	V <sub>cc</sub>	-0.4		3.8	V	VccT may not differ by more than 0.5V

### TABLE 3 OPERATING CONDITIONS

Parameter	Symbol	Min	Тур	Мах	Units	Notes
Operating Temperature, Case	T <sub>op</sub>	-40		+85	°C	
Supply Voltage	V <sub>cc</sub>	3.135	3.3	3.465	V	
Supply Current	lcc		180	200	mA	Typical @ +85°C
Power Supply Noise (Peak-Peak)	V <sub>cc_ripple</sub>			100	mV	

### TABLE 4 ELECTRO-OPTICAL CHARACTERISTICS – TRANSMITTER

Parameter	Symbol	Min	Тур	Max	Units	Notes
Optical Output Power	P <sub>OUT</sub>	-8	-5	-3	dBm	850nm VCSEL, 1.5Gbps
Extinction Ratio	Er	5	10		dB	@1.5Gbps
Optical Wavelength	$\lambda_{OUT}$	830	850	860	nm	
Spectral Width, rms	Δλ			0.85	nm	
Relative Intensity Noise	RIN			-117	dB/Hz	
Timing Jitter	TJ			1	UI	SMPTE 292, 1.5 Gbps, Color bar
Alignment Jitter	AJ			0.2	UI	SMPTE 292, 1.5 Gbps, Color bar
Transmitter Differential Input Impedance	Z <sub>in</sub>		100		Ohms	Requires external AC coupling on customer's board
Differential Input Voltage	V <sub>in_d</sub>	250		2200	mV <sub>p-p</sub>	CML, 100 ohm

## Ratings and Specifications – 3G-SDI VERSION

#### TABLE 5 ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Тур	Мах	Units	Notes
Storage Temperature	Ts	-55		+100	°C	
Supply Voltage	$V_{cc}$	-0.4		3.8	V	VccT may not differ by more than 0.5V

#### TABLE 6 OPERATING CONDITIONS

Parameter	Symbol	Min	Тур	Max	Units	Notes
Operating Temperature, Case	T <sub>op</sub>	-40		+85	°C	
Supply Voltage	V <sub>cc</sub>	3.135	3.3	3.465	V	
Supply Current	Icc		180	200	mA	Typical @ +85°C
Power Supply Noise (Peak-Peak)	V <sub>cc_ripple</sub>			100	mV	

TABLE 7 ELECTRO-OPTICAL CHARACTERISTICS – TRANSMITTER							
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Parameter	Symbol	Min	Тур	Max	Units	Notes
Optical Output Power	Pout	-8	-5	-3	dBm	850nm VCSEL, 2.97Gbps
Extinction Ratio	Er	5	10		dB	@2.97Gbps
Optical Wavelength	$\lambda_{OUT}$	830	850	860	nm	
Spectral Width, rms	Δλ			0.85	nm	
Relative Intensity Noise	RIN			-117	dB/Hz	
Timing Jitter	ТJ			2	UI	SMPTE 424, 2.97Gbps, Color bar
Alignment Jitter	AJ			0.2	UI	SMPTE 424, 2.97Gbps, Color bar
Transmitter Differential Input Impedance	Z <sub>in</sub>		100		Ohms	Requires external AC coupling on customer's board
Differential Input Voltage	V <sub>in_d</sub>	250		2200	mV <sub>p-p</sub>	CML, 100 ohm

## Ratings and Specifications – (Continued)

## TABLE 8 COMPLIANCE SPECIFICATIONS

CHARACTERISTIC	Standard	Condition	Notes
ESD	MIL-STD-883		1000V HBM
Eye Safety	CDRH and IEC-825	Class 1 Laser Product	

## Input/Output Definition

## TABLE 9 TRANSMITTER ELECTRICAL PIN ARRANGEMENT

Pin Number	Symbol	Description	Logic
1	TX DISABLE	Transmit Disable (Input), Normally Disabled	CMOS
		Logic "1" Input → Disable Transmitter Output	Internal 5k-10kOhm Pullup
2	Vcc	Power Supply	
3	GND	Signal Ground	
4	TX FAULT	Transmitter Fault Indicator (Output)	CMOS
		Logic "1" Output -> Transmitter Fault Condition	Open Drain
5	TX-	Transmitter Inverted Data (Input)	CML (Current Mode Logic)
6	TX+	Transmitter Non-Inverted Data (Input)	CML (Current Mode Logic)

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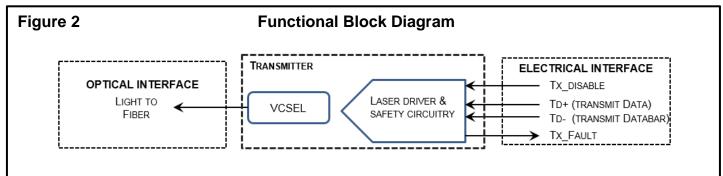
**Outline Drawing** 

See datasheet for more details

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## Functional Description



## FUNCTIONAL I/O

The Size 8 module is compatible with industry standard differential signals such as CML within the scope of the SFP MSA. The module is DC-coupled and internally terminated.

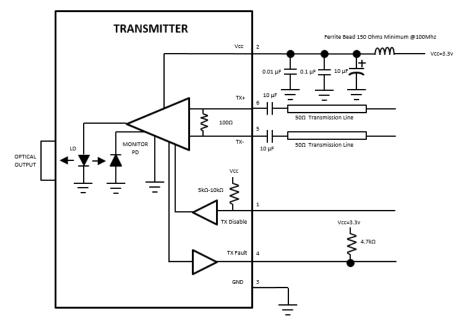
Figure 3 illustrates a recommended interface circuit to size 8 transmitter and the supporting Physical Layer integrated circuits.

The transmitter interfaces with the host circuit board through six I/O pins identified by function in Table 7. The transmitter high speed transmit and interfaces require SFP MSA compliant signal lines on the host board. The TX\_DISABLE, TX\_FAULT require TTL lines on the host board (per SFF-8074i) if used. If an application chooses not to take advantage of the functionality of these pins TX\_Disable need to be tied to GND, TX\_Fault do not need to be connected.

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## Figure 3 Recommended Host Board Schematic



## Required Host Board Components

A power supply noise rejection filter as describe in SFP MSA is required on the host PCB to meet data sheet performance. This filter incorporates an inductor which should be rated 400 mADC and 1  $\Omega$  series resistance or better. It should not be replaced with a ferrite. The required filter is illustrated in Figure 4. Also, the host PCB for the transmitter requires 4.7 K to 10 K $\Omega$  pull-up resistors for TX\_FAULT.

## Fiber Compatibility

The SMPTE link is capable of error free signal detection for 2 to 500 meters with OM3 50/125 µm fiber, 2 to 170 meters with OM2 50/125 µm fiber, and at 2 to 60 meters with OM1 62.5/ 125 µm fiber, for 3G-SDI data rate.

## Electrostatic Discharge (ESD)

The size 8 contacts are compatible with ESD levels found in typical manufacturing and operating environments as described JEDEC EIA JESD22-A114-A, Class 1C (<2000Volts) HBM. Glenair recommends that devices are handled with ESD precautions to limit exposure to below 500V HBM.

There are two design cases in which immunity to ESD damage is important. The first case is during handling of the components prior to insertion to the host board. It is important to use standard industry ESD handling precautions such as using grounded wrist straps, work benches, and floor mats in ESD controlled areas. The ESD sensitivity of the Glenair device is compatible with typical industry production environments.

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The second case to consider is static discharges to the exterior of the host equipment after installation, in which case the electro-optical component may be subject to system-level ESD requirements.

## Application Support

To assist in the transmitter design and evaluation process, Glenair offers the following aids:

- Evaluation board & Product Manual
- 3D Step file to support modeling of mechanical fit and routing

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