



050-377

PRODUCT BRIEF
PRINTED CIRCUIT BOARD (PCB) MOUNT
10GBPS CWDM DUAL-RECEIVER
SMALL & COMPACT WITH RUGGED CONSTRUCTION FOR HARSH ENVIRONMENTS

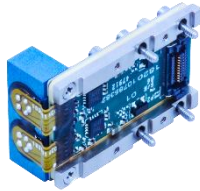
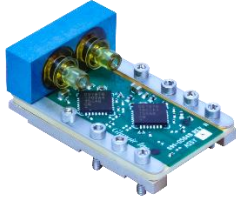
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PCB Mount 10 Gbps Dual-Receiver
SMF, 3.3V



PCB Mount Fiber Optic 10Gbps CWDM Dual-Receiver, SMF, 3.3V



Glenair 050-377, is a ruggedized harsh environment PCB mount 10Gbps Dual-Receiver with electrical and optical functionality equivalent to SFP devices but with mechanical design that is suited to the harsh temperature and vibration environments found in the Military, Aerospace, Railway, Oil and Gas, and Industrial applications. The PCB mount optical transceivers also support Digital Monitoring Interface (DMI) features in accordance with SFF 8472. The Dual-Receiver interfaces with a host board through a high speed electrical connector.

The receiver section consists of a Receiver Optical Subassembly (ROSA) and limiting amplifier to provide quantized CML output signals.

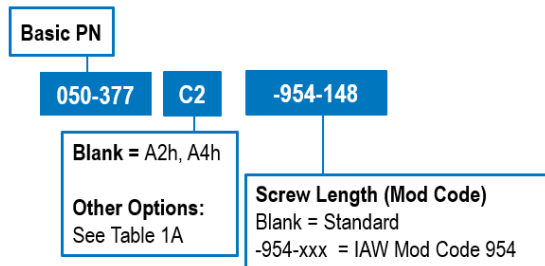
KEY FEATURES/BENEFITS

- SFP Compatible Electrical I/O signal levels
- PIN PD to support high sensitivity up to 10 Gbps
- Industry standard CML outputs that make for simple integration on customer host PCB
- Glenair Rugged GC Optical connector
- Receiver is securely mounted with screws to PCB to ensure excellent shock and vibration performance
- High-Speed Electrical plug-in connector eliminates the need for soldering & enables ease of servicing
- Compact Size: Approx. 0.8" x 0.9" x 0.5"
- -40°C to +85°C Operating Case Temperature
- Glenair fiber jumpers connect from transceiver to any Glenair Mil/Aero Fiber Optic Connector Style
- Evaluation fixtures available
- Digital Diagnostic and Monitoring (DMI) based on SFF-8472

APPLICATIONS

- Harsh Environment such as: Airborne, Tactical, Railway, Industrial, Oil and Gas and Shipboard applications
 - Ethernet, Fibre channel, 1x, 2x, 4x, 8x SFPDP

How To Order



Example: **050-377**

PCB Mount CWDM Dual-Receiver, SMF,, Two Wire Address = A2h & A4h, Data Rate = 10 Gbps, Standard Screw Length

**Temperature and Screw Length Mod Codes will not be added onto Digital Memory ID (See Table 7)

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TABLE 1A TWO WIRE ADDRESS –IDENTIFIER OPTIONS

PN Identifier	Transceiver #1 Address	Transceiver #2 Address
Blank	A2h	A4h
A6	A6h	A8h
B2	B2h	B4h
B6	B6h	B8h
C2	C2h	C4h
C6	C6h	C8h
D2	D2h	D4h
D6	D6h	D8h
E2	E2h	E4h
E6	E6h	E8h

**Contact Glenair if additional addresses required*

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Ratings and Specifications

TABLE 2 OPERATING CONDITIONS

Parameter	Symbol	Min	Typ	Max	Units	Notes
Operating Temperature, Case	T _{op}	-40		+85	°C	
Supply Voltage	V _{cc}	3.135	3.3	3.465	V	
Supply Current	I _{cc}		180	270	mA	@ +85°C
Power Supply Noise (Peak-Peak)	V _{cc_ripple}			5	mV	Per SFF-8431 Rev 4.1

TABLE 3 ELECTRO-OPTICAL CHARACTERISTICS - RECEIVER

Parameter	Symbol	Min	Typ	Max	Units	Notes
Sensitivity, BER 10 ⁻¹² , PRBS31, Er 5 dB	P _{IN}		-16	-15	dBm	PIN PD @ 10.3 Gbps
LOS Assert Level	LOS _A	-30	-19		dBm	@ 10.3 Gbps
LOS Hysteresis	LOS _{HYS}	0.5	2.0		dB	
Overload, BER 10 ⁻¹² , PRBS31	P _{IN}	+1			dBm	
Optical Wavelength	λ _{IN}	1260		1620	nm	
Receiver Differential Output Impedance	R _{out}		100		Ohms	AC coupled internally
Differential Output Voltage Swing	V _{out_d}	300		850	mV _{p-p}	CML, 100 ohm

TABLE 4 COMPLIANCE SPECIFICATIONS

CHARACTERISTIC	Standard	Condition	Notes
Mechanical Shock	MIL-STD-810	Para. 516.6, proc. I, 650g	0.9 ms operating
Mechanical Vibration	MIL-STD-810	Para. 514.6, 40g rms	Random, operating
ESD	MIL-STD-883		500V HBM
Flame Resistance	MIL-STD-1344	Method 1012, Cond. B	30 seconds
Damp Heat	MIL-STD-1344	Method 1002.2, Cond. B	10 cycles , 24 hours
Eye Safety	CDRH and IEC-825	Class 1 Laser Product	

TABLE 5 MATERIAL/FINISH

Item	Material/Finish
PCB	FR4
PCB flex	FR4 & Polyimide
Railings	Aluminum 6061-T6 per ASTM-B221/B211M or Equivalent
Screws	CRES Type, 302, 303, 304, 305, or 316
Optical Ferrules & Sleeves	Zirconia, Ceramic
Solder type	RoHS compliant Sn95/Sb5 (232°C melting temp) & RoHS compliant Sn96.5/Ag3.0/Cu0.5 (217° melting)

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Input/Output Definition

SEE DATA SHEET FOR MORE DETAILS

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FIGURE 1 - OUTLINE DRAWING

See Datasheet for more details

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FIGURE 1 - OUTLINE DRAWING CONTINUED (MARKING)

LABELING:

Each unit will be shipped in an antistatic bag. The label on the antistatic bag shall be at a minimum Arial size 10 black font and contain at a minimum the following information:

ANTISTATIC BAG LABEL:

Glenair

Cage Code: 06324

PN: 050-377XX-XXXX-XXX

Rev: X

QTY: X

J/N: X

D/C: X

S/N*: XXXXXX

*If QTY is more than 1, there is no S/N

Each unit will be marked on the transceiver as follows. Either laser engraving or labeling may be used.

- Marking font to be Arial, greater than .08 inches in height.

- Marking:

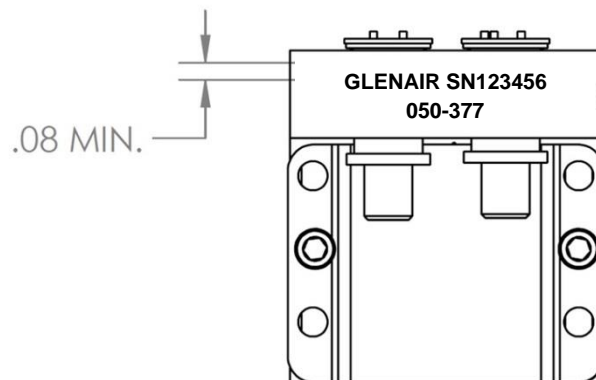
FIRST LINE OF TEXT

- Glenair
- Serial Number (6 digits)

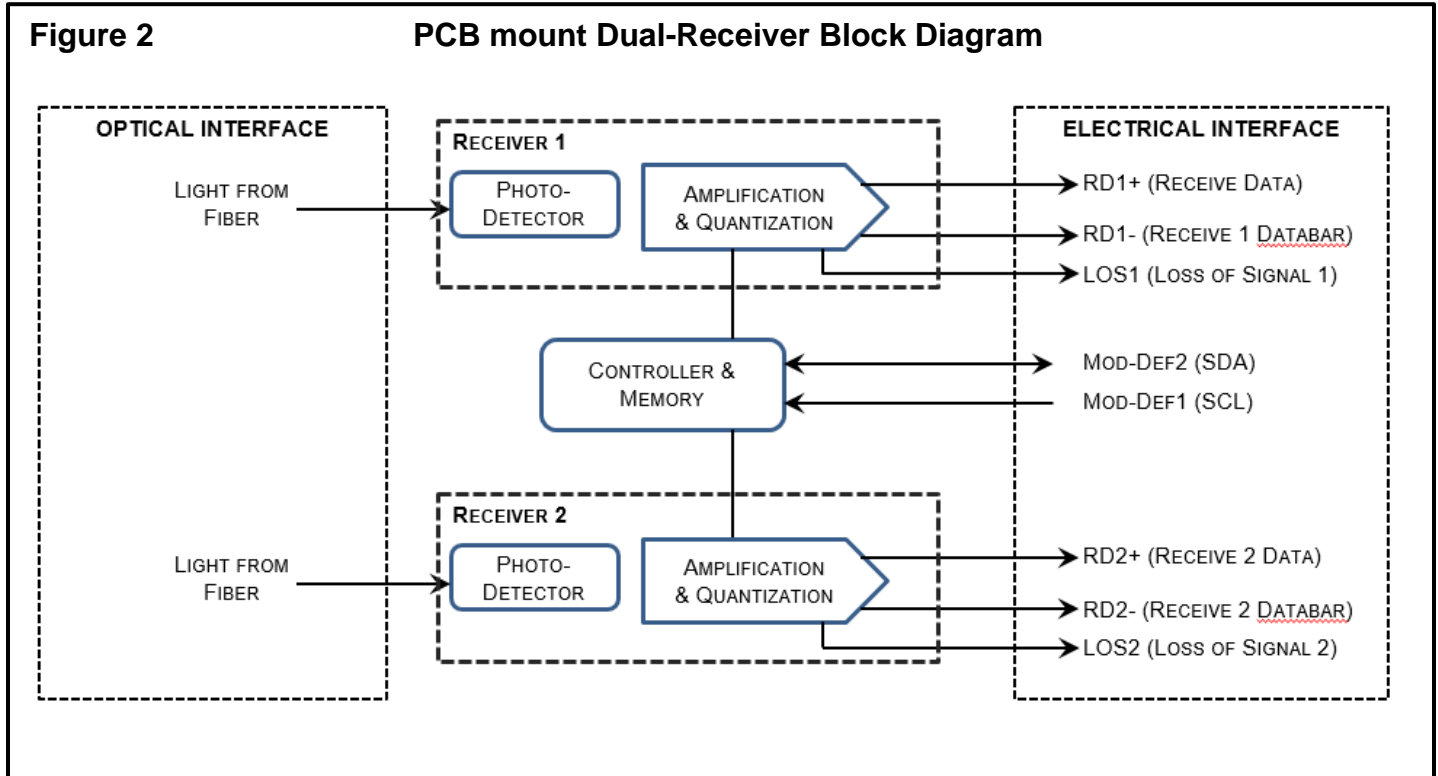
SECOND LINE OF TEXT:

- Part number

Example



FUNCTIONAL DESCRIPTION



RECEIVER SECTION

Receiver Loss of Signal (LOS)

The Loss Of Signal (LOS) output indicates an unusable optical input power level. The post-amplification IC includes transition detection circuitry which monitors the ac level of incoming optical signals and provides a TTL/CMOS compatible status signal to the host. A low LOS logic level indicates the presence of an optical input while a high LOS logic level indicates an unusable optical input. The LOS thresholds are factory-set so that a high output indicates a definite optical fault has occurred (e.g. failed transmitter, broken or disconnected fiber connection to the PCB Mount Module, etc.). The LOS can also be monitored via the 2-wire serial interface (address A2h and A4h, byte 110, bit 1).

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RECEIVER CML OUTPUT VOLTAGE AMPLITUDE AND DEEMPHASIS CONTROL

CML output voltage amplitude, deemphasis, and rate select for each receiver of 057-377 dual receiver can be adjusted through the I2C serial interface (pins 13 and 15). Three configuration registers RXCTRL1, RXCTRL2, and SET_CML, set CML output amplitude, deemphasis, and rate select of the receiver.

Register Address	Register Function	Location
00h, RXCTRL1	Receiver Control	Table 02h, E8h
01h, RXCTRL2	Receiver Control	Table 02h, E9h
03h, SET_CML	Output CML Level Setting	Table 02h, EAh

Table select register 7Fh, I2C address for 050-377 (A2h/A4h), or 050-377-B2 (B2h/B4h), or 050-377-B6 (B6h/B8h),

CML Output Amplitude Range (Typical)

RXCTRL1 [1]	RXCTRL2 [1]	RXCTRL1 [7:6]		MODE	OUTPUT AMPLITUDE (mVp-p)
RATE_SEL	RXDE_EN	RXDE1	RXDE0		
1	0	X	X	High data-rate path	400 to 1147
1	1	0	0	High data -rate path with deemphasis	400 to 1041
1	1	0	1	High data -rate path with deemphasis	400 to 987
1	1	1	0	High data -rate path with deemphasis	400 to 908
1	1	1	1	High data -rate path with deemphasis	400 to 828

Contact factory if lower data rate receivers required.

Output Signal Deemphasis Control (Typical)

RXCTRL1 [1]	RXCTRL1 [7:6]		MODE	DEEMPHASIS (dB)
RXDE_EN	RXDE1	RXDE0		
0	X	X	Deemphasis block is disabled	0
1	0	0	Deemphasis block is enabled level 1	0.3
1	0	1	Deemphasis block is enabled level 2	1.1
1	1	0	Deemphasis block is enabled level 3	2.1
1	1	1	Deemphasis block is enabled level 4	4.3

CML Output DAC Resolution (Typical)

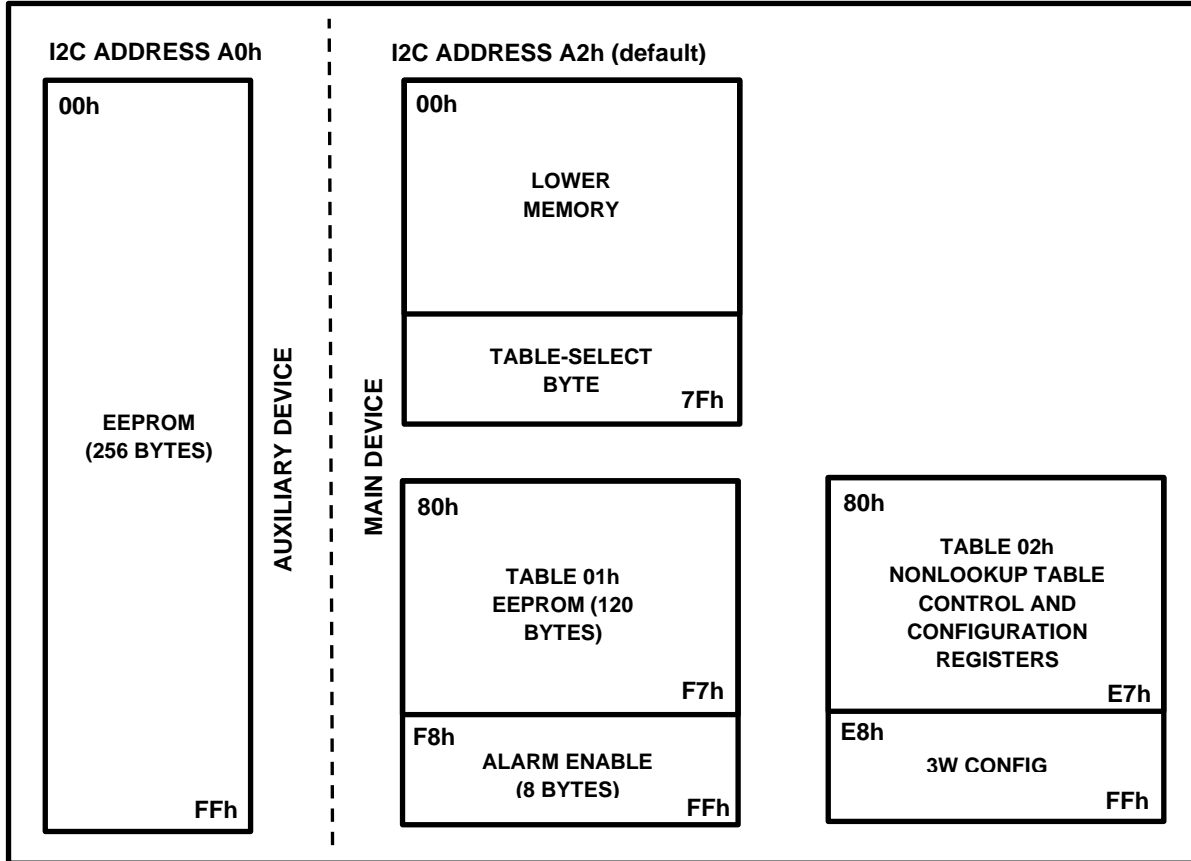
RXCTRL1 [1]	RXCTRL2 [1]	RXCTRL1 [7:6]		MODE	RESOLUTION (mVp-p)
RATE_SEL	RXDE_EN	RXDE1	RXDE0		
0	X	X	X	Low data-rate path	4.5
1	0	X	X	High data-rate path	4.5
1	1	0	0	High data -rate path with deemphasis	4.1
1	1	0	1	High data -rate path with deemphasis	3.9
1	1	1	0	High data -rate path with deemphasis	3.6
1	1	1	1	High data -rate path with deemphasis	3.3

SET_CML DAC Range is 60 to 255 Decimal.

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Memory Map



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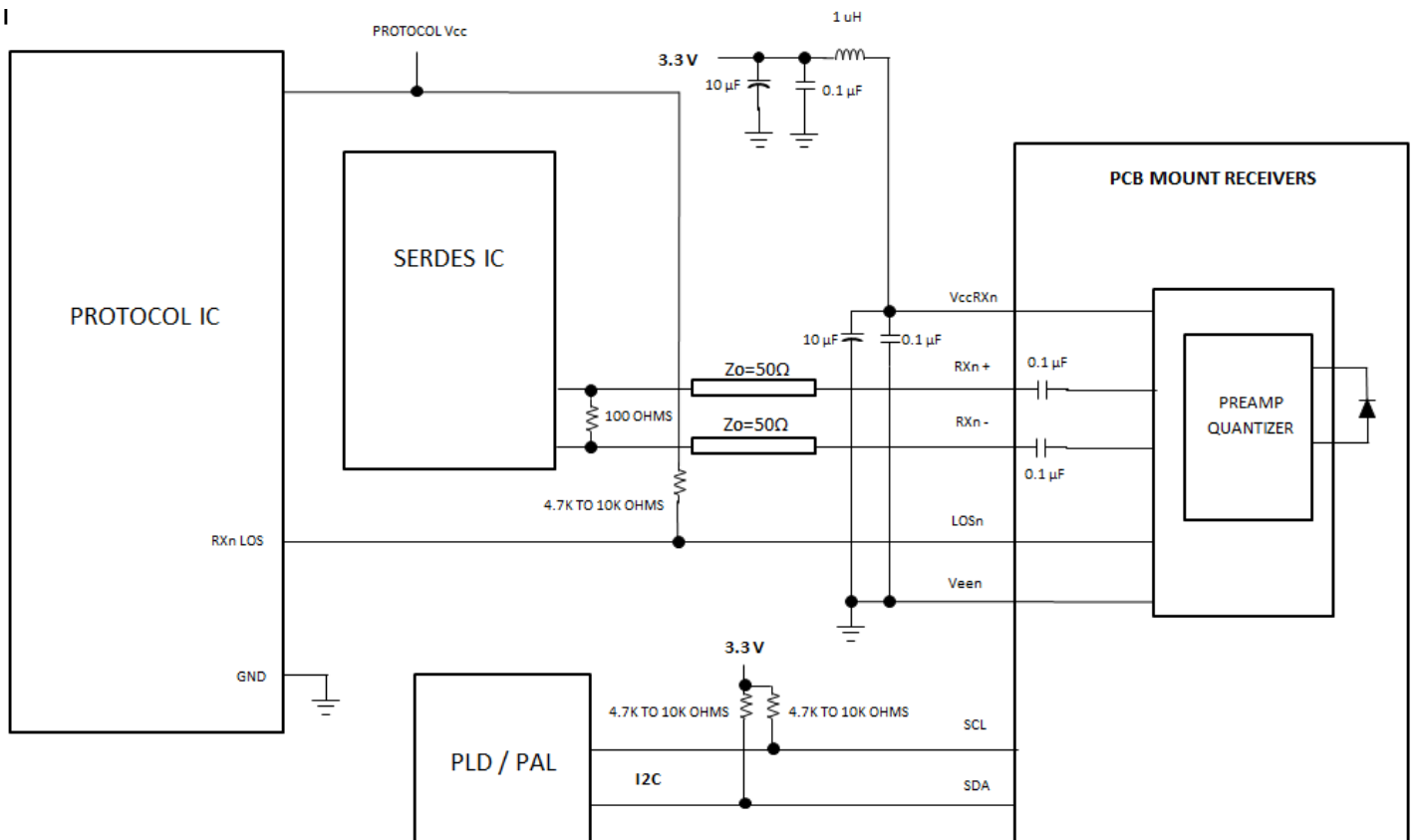


FUNCTIONAL I/O

The PCB mount Dual-Receiver accepts industry standard differential signals such as LVPECL and CML within the scope of the SFP MSA. The module is AC-coupled and internally terminated.

Figure 3 illustrates a recommended interface circuit to link the PCB mount Dual-Receiver to the supporting Physical Layer integrated circuits.

**FIGURE 3 RECOMMENDED PCB MOUNT DUAL-RECEIVER HOST BOARD SCHEMATIC
 SINGLE RECEIVER SHOWN (2X RECEIVERS PER MODULE)**



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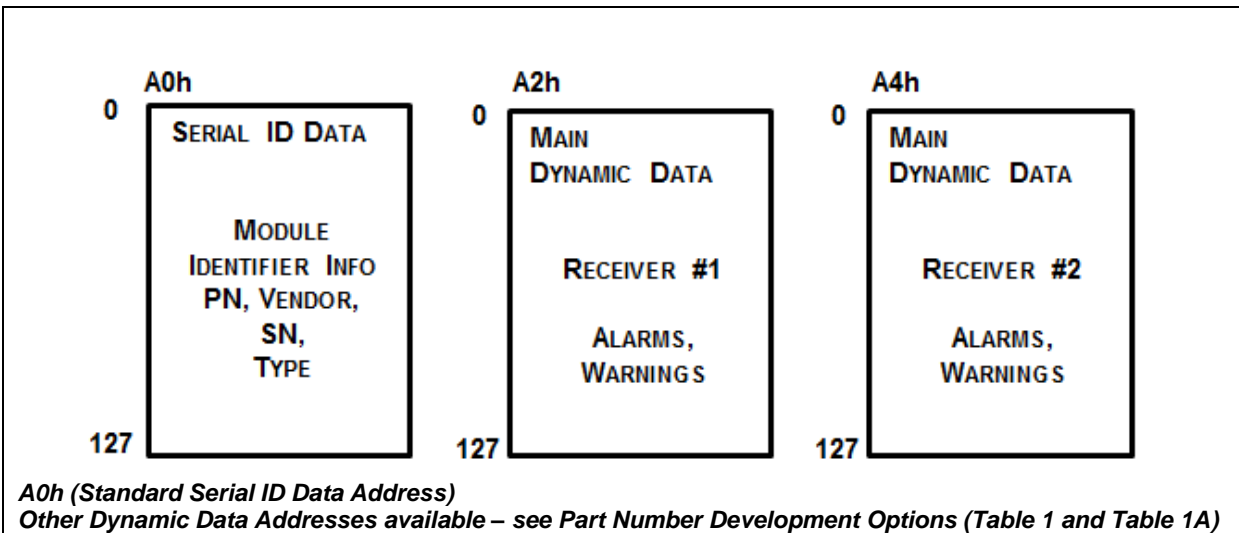


The PCB mount Dual-Receiver interfaces with the host circuit board through twenty I/O pins identified by function in Table 7. The Dual-Receiver high speed receive interface requires SFP MSA compliant signal lines on the host board. The Rx_LOS lines require TTL lines on the host board (per SFF-8074i) if used. If an application chooses not to take advantage RX_LOS do not need to be connected.

Digital Diagnostic Interface and Serial Identification (EEPROM)

The PCB mount Dual-Receiver is derived from the SFF-8074i SFP specification and with SFF-8472, the SFP specification for Digital Diagnostic Monitoring Interface, but modified to account for Dual-Receiver functionality. Both specifications can be found at <http://www.sffcommittee.org>.

FIGURE 4 – DIGITAL DIAGNOSTIC MEMORY MAP OVERVIEW



The PCB mount Dual-Receiver features EEPROM for Serial ID, which contains the product data stored for retrieval by host equipment. This data is accessed via the 2-wire serial EEPROM derived from with the industry standard SFP Multi-Source Agreement but modified to account for Dual-Receiver functionality. The base EEPROM memory, bytes 0-255 at memory address A0h, is organized in compliance with SFF-8074i.

As an enhancement to the conventional SFP interface defined in SFF-8074i, the PCB mount Dual-Receiver is derived from SFF-8472 (digital diagnostic interface for optical transceivers) with memory organized as shown in Figure 4. This new digital diagnostic information is stored in bytes 0-255 at memory address A2h (RX1) and A4h (RX2). Using the 2-wire serial interface defined in the MSA, the PCB mount Dual-Receiver provides real time temperature, supply voltage, and received input power. These parameters are internally calibrated, per the SFF-8472 MSA. The digital diagnostic interface also adds the capability to monitor for Receiver Loss of Signal (RX_LOS). The diagnostic information provides the opportunity for Predictive Failure Identification, Compliance Prediction, Fault Isolation and Component Monitoring.

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Compliance Prediction

Compliance prediction is the ability to determine if an optical Dual-Receiver is operating within its operating and environmental requirements. The PCB mount Dual-Receiver provides real-time access to its internal supply voltage and temperature, allowing a host to identify potential component compliance issues. Received optical power is also available to assess compliance of fiber cable plant and remote transmitter. When operating out of requirements, the link cannot guarantee error free transmission.

Fault Isolation

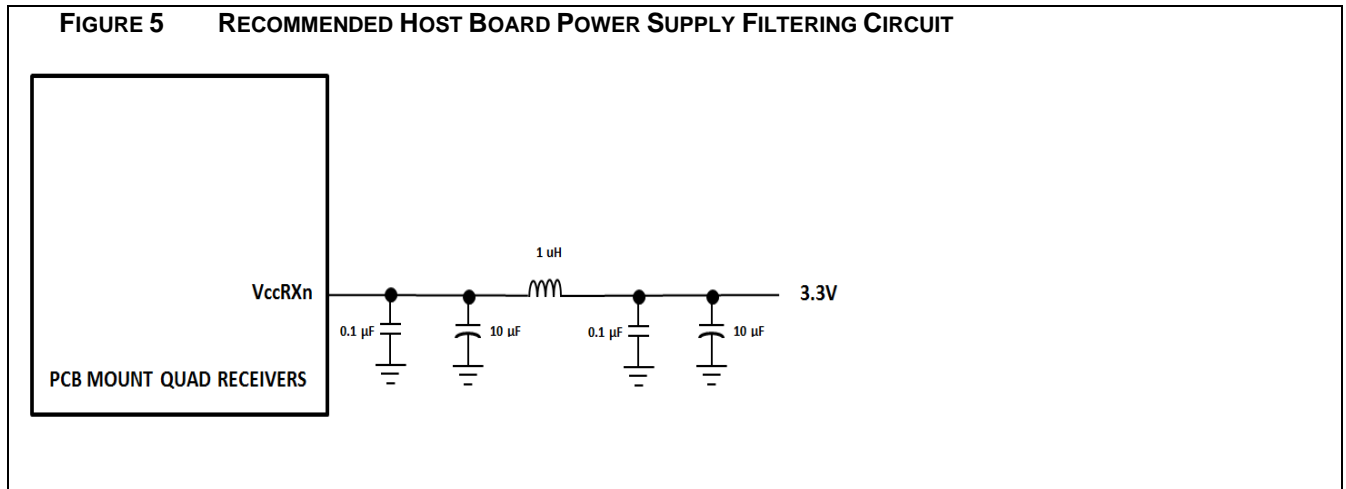
The fault isolation feature allows a host to quickly pinpoint the location of a link failure, minimizing downtime. For optical links, the ability to identify a fault at a local device, remote device or cable plant is crucial to speeding service of an installation. PCB mount Dual-Receiver real-time monitors of Vcc, Temperature and Rx_Power can be used to assess local Dual-Receiver current operating conditions. In addition, status flag Rx Loss of Signal (LOS) is mirrored in memory and available via the two-wire serial interface.

Component Monitoring

Component evaluation is another use of the PCB mount Dual-Receiver real-time monitors of Vcc, Temperature and Rx_Power. Potential uses are as debugging aids for system installation and design, and Dual-Receiver parametric evaluation for factory or field qualification. For example, temperature per module can be observed in high density applications to facilitate thermal evaluation of systems that incorporate this PCB mount Dual-Receiver.

Required Host Board Components

A power supply noise rejection filter as describe in SFP MSA is required on the host PCB to meet data sheet performance. This is filter incorporates an inductor which should be rated to 400 mADC and 1 Ω series resistance or better. It should not be replaced with a ferrite. The required filter is illustrated in Figure 5. Also, the host PCB for the PCB mount Dual-Receiver requires 4.7 K to 10 K Ω pull-up resistors for LOS, SCA and SDL lines.



Fiber Compatibility

The Dual-Receiver is capable of transmission at 2 to 550 meters with 50/125 μm fiber, and at 2 to 275 meters with 62.5/125 μm fiber, for 1.25 GBd Ethernet. It is capable of transmission up to 550m with 50/125 μm fiber and up to 300m with 62.5/125 μm fiber, for 1.0625 GBd Fiber Channel.

Electrostatic Discharge (ESD)

The Dual-Receiver is compatible with ESD levels found in typical manufacturing and operating environments as described JEDEC EIA JESD22-A114, Class 1C (<2000Volts) HBM. Glenair recommends that devices are handled with ESD precautions to limit exposure to below 500V HBM.

There are two design cases in which immunity to ESD damage is important. The first case is during handling of the Dual-Receiver prior to insertion to the host board. To protect the Dual-Receiver, it's important to use standard industry ESD handling precautions. These precautions include using grounded wrist straps, work benches, and floor mats in ESD controlled areas. The ESD sensitivity of the Glenair PCB mount Dual-Receiver is compatible with typical industry production environments.

The second case to consider is static discharges to the exterior of the host equipment after installation, in which case the Dual-Receiver may be subject to system-level ESD requirements.

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Application Support

To assist in the dual-transmitter design and evaluation process, Glenair offers the following aids:

- Evaluation board & Product Manual, part number 050-330-MMF-EVALBOARD, which facilitates in the testing of the board mount dual-transmitter.
- 3D Step file to support modeling of mechanical fit and routing
- PADS schematic and PCB layout library files that can be exported into customer's PCB software design program
- Applications Aid Example of PCB layout including details of high speed transmission designs

Customer Manufacturing Processes

This module is mounted with screws and interfaces with a high-speed low cost surface mount electrical connector residing on the host PC board. The board mount dual-transmitter is not designed for aqueous wash, IR reflow, or wave soldering processes and should be mounted on the host board after host PC board has been through its assembly process.

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TABLE 7 TWO-WIRE INTERFACE ID: DATA FIELDS – ADDRESS A0h

Byte Decimal	# Hex	Data Notes	Byte Decimal	# Hex	Data Notes
0	80	Glenair PCB mount Transceiver	37	00	Vendor OUI (NOT USED)
1	04	Serial ID Module Definition	38	00	Vendor OUI (NOT USED)
2	80	"GC" Fiber Optic Connector	39	00	Vendor OUI (NOT USED)
3	00		40	30	"0" Vendor Part Number ASCII character (Note 5)
4	00		41	35	"5" Vendor Part Number ASCII character (Note 5)
5	00		42	30	"0" Vendor Part Number ASCII character (Note 5)
6	00		43	2D	“-” Vendor Part Number ASCII character (Note 5)
7	00		44	33	"3" Vendor Part Number ASCII character (Note 5)
8	00		45	37	"7" Vendor Part Number ASCII character (Note 5)
9	00		46	36	"7" Vendor Part Number ASCII character (Note 5)
10	00		47	43	"C" Vendor Part Number ASCII character (Note 5)
11	06	Compatible with 64/66B encoded data	48	32	"2" Vendor Part Number ASCII character (Note 5)
12	67	BR, 10.3125 Gbps	49	2D	“-” Vendor Part Number ASCII character (Note 5)
13	00		50	32	"2" Vendor Part Number ASCII Character (Note 5)
14	00		51	37	"7" Vendor Part Number ASCII Character (Note 5)
15	64	9/125 μm Fiber, 10km @ 1.25Gbps	52	32	"2" Vendor Part Number ASCII Character (Note 5)
16	00		53	39	"9" Vendor Part Number ASCII Character (Note 5)
17	00		54	20	" " Vendor Part Number ASCII Character (Note 5)
18	00		55	20	" " Vendor Part Number ASCII Character (Note 5)
19	00		56	20	" " Vendor REV Level ASCII Character (Note 5)
20	47	"G" Vendor NAME ASCII Character	57	20	" " Vendor REV Level ASCII Character (Note 5)
21	4C	"L" Vendor NAME ASCII Character	58	20	" " Vendor REV Level ASCII Character (Note 5)
22	45	"E" Vendor NAME ASCII Character	59	20	" " Vendor REV Level ASCII Character (Note 5)
23	4E	"N" Vendor NAME ASCII Character	60	04	Hex Byte of Laser Wavelength (Note 6)
24	41	"A" Vendor NAME ASCII Character	61	F6	Hex Byte of Laser Wavelength (Note 6)
25	49	"I" Vendor NAME ASCII Character	62		RESERVED
26	52	"R" Vendor NAME ASCII Character	63		Check sum code for ID fields 0-62 (Note 7)
27	20	"" Vendor NAME ASCII Character	64	00	
28	20	"" Vendor NAME ASCII Character	65	02	RX_ LOS
29	20	"" Vendor NAME ASCII Character	66	00	
30	20	"" Vendor NAME ASCII Character	67	00	
31	20	"" Vendor NAME ASCII Character	68-83		Serial Number, ASCII (Note 8)
32	20	"" Vendor NAME ASCII Character	84-91		Date Code (Note 9)
33	20	"" Vendor NAME ASCII Character	92	68	Diagnostic Monitoring Type
34	20	"" Vendor NAME ASCII Character	93	B0	Enhanced Options
35	20	"" Vendor NAME ASCII Character	94	05	SFF-8472 rev 11.0
36	00		95		Checksum for bytes 64-94 (Note 7)
			96-255	00	

Notes:

1. RESERVED
2. RESERVED
3. RESERVED
4. RESERVED
5. Table 1, Part number options/extensions. Temperature and Screw Length Mod Codes will not be added onto Digital Memory ID.
6. Laser wavelength is represented in 16 unsigned bits. The hex representation of 1310 nm is 051E. The hex representation of 850nm is 0352.
7. Addresses 63 and 95 are checksums calculated per SFF-8472 and SFF-8074, and stored prior to product shipment.
8. Addresses 68-83 specify the module's ASCII serial number and will vary by unit.
9. Addresses 84-91 specify the module's ASCII date code and will vary according to manufactured date-code.

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Table 8 Two-Wire interface ID: Data Fields – Address A2h (RX1), A4h (RX2) standard (or see Table 1A for other options)
NOTE: TX Data fields are not applicable (i.e. address 16-31 and 100-103 are not applicable)

Byte # Decimal	Data Notes	Byte # Decimal	Data Notes	Byte # Decimal	Data Notes
0	Temp H Alarm MSB (Note 1)	26	Reserved	104	Rx Pavg MSB (Note 5)
1	Temp H Alarm LSB (Note 1)	27	Reserved	105	Rx Pavg LSB (Note 5)
2	Temp L Alarm MSB (Note 1)	28	Reserved	106	Reserved
3	Temp L Alarm LSB (Note 1)	29	Reserved	107	Reserved
4	Temp H Warning MSB (Note 1)	30	Reserved	108	Reserved
5	Temp H Warning LSB (Note 1)	31	Reserved	109	Reserved
6	Temp L Warning MSB (Note 1)	32	RX Pwr H Alarm MSB (Note 5)	110	Status/Control
7	Temp L Warning LSB (Note 1)	33	RX Pwr H Alarm LSB (Note 5)	111	Reserved
8	Vcc H Alarm MSB (Note 2)	34	RX Pwr L Alarm MSB (Note 5)	112	Flag Bits
9	Vcc H Alarm LSB (Note 2)	35	RX Pwr L Alarm LSB (Note 5)	113	Flag Bits
10	Vcc L Alarm MSB (Note 2)	36	RX Pwr H Warning MSB (Note 5)	114	Reserved
11	Vcc L Alarm LSB (Note 2)	37	RX Pwr H Warning LSB (Note 5)	115	Reserved
12	Vcc H Warning MSB (Note 2)	38	RX Pwr L Warning MSB (Note 5)	116	Flag Bits
13	Vcc H Warning LSB (Note 2)	39	RX Pwr L Warning LSB (Note 5)	117	Flag Bits
14	Vcc L Warning MSB (Note 2)	40-45	Reserved	118	Reserved
15	Vcc L Warning LSB (Note 2)	56-94	External Cal Constants	119	Reserved
16	Reserved)	95	Checksum for bytes 0-94	120-122	Reserved
17	Reserved	96	Temperature MSB (Note 1)	123	Reserved
18	Reserved	97	Temperature LSB (Note 1)	124	Reserved
19	Reserved	98	Vcc MSB (Note 2)	125	Reserved
20	Reserved	99	Vcc LSB (Note 2)	126	Reserved
21	Reserved	100	Reserved	127	Reserved (Note 8)
22	Reserved	101	Reserved	128-247	Customer Writable (Note 9)
23	Reserved	102	Reserved	248-255	Vendor Specific
24	Reserved	103	Reserved		
25	Reserved				

Notes:

1. Temperature (Temp) is decoded as a 16 bit signed twos complement integer in increments of 1/256 °C.
2. Supply voltage (VCC) is decoded as a 16 bit unsigned integer in increments of 100 µV.
3. RESERVED
4. RESERVED.
5. Received average optical power (Rx Pwr) is decoded as a 16 bit unsigned integer in increments of 0.1 µW.
6. Bytes 55-94 are not intended from use but have been set to default values per SFF-8472.
7. Bytes 95 is a checksum calculated (per SFF-8472) and stored prior to product shipment.
8. Byte 127 accepts a write but performs no action (reserved legacy byte).
9. Bytes 128-247 are write enabled (customer writable).

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TABLE 9 TRANSCIVER DIGITAL DIAGNOSTIC MONITOR CHARACTERISTICS (WHEN APPLICABLE)

PARAMETER	SYMBOL	MIN.	UNITS	NOTES
Dual-Receiver Internal Temperature Accuracy	TINT	±3.0	°C	Temperature is measured internal to the Dual-Receiver and is valid from -40°C to +85 °C case temperature
Dual-Receiver internal Supply Voltage accuracy	VINT	±0.1	V	Supply voltage is measured internal to the Dual-Receiver and can, with less accuracy, be correlated to the voltage at the Vcc pin. Valid over 3.3V ±5%
Received Average Optical Input Power Accuracy	PR	±3.0	dB	Coupled from 50/125 mm MM fiber Valid from -24 dBm to - 1 dBm

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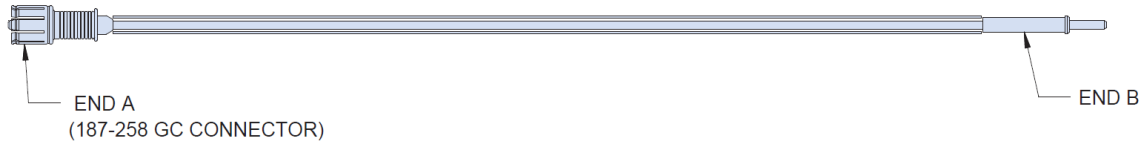
ACCESSORIES

Fiber Optic Jumper Cables to support connection to Mil/Aerospace Connectors

KEY FEATURES:

- Jumper cable between Glenair Transceiver (end A) and Mil/Aero Connector termini (End B)
- Offered with either Multimode and Single Mode fibers

FIGURE 5
Fiber Optic Jumper Cable Assembly (see separate Glenair sales drawing FA02454 for details)



PCB Threaded Inserts, (PN 990-05017-1) sold as a kit of 100 pcs under kit part number 059-0007-1

KEY FEATURES:

- Simplifies installation of PCB Mount transceivers eliminating the need for washers and nuts.
- Soldered to PCB to eliminate need for handling nuts during assembly.

EVALUATION Boards, PN 050-330 Include

- MANUAL with test block diagram, schematic and Evaluation board PCBA
- 2 fiber optic cables
- See 050-330 datasheet for more details

